

AT800 System Test Coverage for WCDMA/HSDPA

3GPP TS34.121-1, Band I, II, V, VIII

	R99 W-CDMA Transmitter Characteristics
5.2	Maximum Output Power (Thermal and CH power)
5.3	Frequency Error
5.4.1	Open Loop Power Control in the Uplink
5.4.2	Inner Loop Power Control in the Uplink
5.4.3	Minimum Output Power
5.4.4	Out-of-synchronization handling of output power
5.5.2	Transmit ON/OFF Time mask
5.6	Change of TFC
5.7	Power Setting in uplink compressed mode
5.8	Occupied Bandwidth (OBW)
5.9	Spectrum emission mask
5.10	Adjacent Channel Leakage Power Ratio (ACLR)
5.11	Spurious Emissions
5.12	Transmit Intermodulation
5.13.1	Error Vector Magnitude (EVM)
5.13.2	Peak code domain error
	R99 W-CDMA Receiver Characteristics
6.2	Reference Sensitivity Level
6.3	Maximum Input Level
6.4	Adjacent Channel Selectivity (ACS)
6.5	Blocking Characteristics
6.6	Spurious Response
6.7	Intermodulation Characteristics
6.8	Spurious emissions
	R99 W-CDMA Performance Requirements
7.2.1	Demodulation of Dedicated Channel (DCH)
7.3.1	Demodulation of DCH in Multi-path Fading Propagation conditions
7.4.1	Demodulation of DCH in Moving Propagation conditions
7.5.1	Demodulation of DCH in Birth-Death Propagation conditions
	Rel-5 W-CDMA/HSDPA Transmitter Characteristics
5.2A	Maximum Output Power with HS-DPCCH
5.7A	HS-DPCCH Power Control
5.9A	Spectrum emission mask with HS-DPCCH
5.10A	Adjacent Channel Leakage Power Ratio (ACLR) with HS-DPCCH

5.13.1A	Error Vector Magnitude (EVM) with HS-DPCCH
5.13.3	UE Phase Discontinuity
5.13.4	PRACH Preamble Quality
	Rel-5 W-CDMA/HSDPA Receiver Characteristics
6.3A	Maximum Input Level for HS-PDSCH Reception (16QAM)
6.4A	Adjacent Channel Selectivity (ACS)
	Rel-5 HSDPA Performance Requirements
9.2.1A	Demodulation of HS-DSCH - Single Link Performance - QPSK/16QAM FRC H-set 1/2/3
9.2.1B	Demodulation of HS-DSCH - Single Link Performance - QPSK FRC H-set 4/5
	Rel-6 W-CDMA/HSDPA Transmitter Characteristics
5.2AA	Maximum Output Power with HS-DPCCH (Rel-6 and later)
5.2C	UE Relative Code Domain Power Accuracy
5.13.1AA	Error Vector Magnitude (EVM) and phase discontinuity with HS-DPCCH
5.13.2A	Relative Code Domain Error with HS-DPCCH
	Rel-6 HSDPA Performance Requirements
9.2.1C	Demodulation of HS-DSCH - Single Link Performance - QPSK/16QAM FRC H-set 6/2
9.2.1F	Demodulation of HS-DSCH - Single Link Performance - Enhanced Performance Requirements Type 2- QPSK FRC H-set 6/3
	Rel-6 W-CDMA/HSPA Transmitter Characteristics
5.2B	Maximum Output Power with HS-DPCCH and E-DCH
5.2D	UE Relative Code Domain Power Accuracy for HS-DPCCH and E-DCH
5.9B	Spectrum Emission Mask with E-DCH
5.10B	Adjacent Channel Leakage Power Ratio (ACLR) with E-DCH
5.13.2B	Relative Code Domain Error with HS-DPCCH and E-DCH
	Rel-6 HSUPA Performance Requirements
10.2.1.1	Detection of E-DCH HARQ ACK Indicator Channel (E-HICH) / Single Link Performance (10ms TTI)
10.4.1	Detection of E-DCH Absolute Grant Channel (E-AGCH) / Single Link Performance
	Rel-7 W-CDMA/HSDPA Receiver Characteristics
6.3B	Maximum Input Level for HS-PDSCH Reception (64QAM)