

AT800 System Test Coverage for TD-SCDMA

3GPP TS 34.122, Band B 2010~2025MHz

	TD-SCDMA Transmitter Characteristics
5.2	UE Maximum Output Power
5.3	UE Frequency stability
5.4	Output power dynamics
5.4.1	Uplink Power control
5.4.1.3	Open loop power control
5.4.1.4	Closed loop power control
5.4.2	Minimum output power
5.4.3	Transmit OFF power
5.4.4	Transmit ON/OFF Time Mask
5.4.5	Out-of-synchronization handling of output power for continuous transmission
5.4.6	Out-of-synchronization handling of output power for discontinuous transmission
5.5	Output RF spectrum emissions
5.5.1	Occupied bandwidth
5.5.2	Out of band emission
5.5.2.1	Spectrum emission mask
5.5.2.2	Adjacent channel leakage power ratio (ACLR)
5.5.3	Spurious emissions
5.6	Transmit inter-modulation
5.7	Transmit modulation
5.7.1	Error Vector Magnitude
5.7.2	Peak Code Domain Error
	TD-SCDMA Receiver Characteristics
6.2	Reference Sensitivity Level
6.3	Maximum Input Level
6.4	Adjacent Channel Selectivity (ACS)
6.5	Blocking Characteristics
6.6	Spurious Response
6.7	Inter-modulation Characteristics
6.8	Spurious emissions
	TD-SCDMA Performance Requirements
7.2	Demodulation in static propagation conditions

7.3	Demodulation of DCH in multi-path fading conditions
7.3.1	Demodulation of DCH in multi-path fading Case1
7.3.2	Demodulation of DCH in multi-path fading Case2
7.3.4	Demodulation of DCH in multi-path fading Case3
7.5	Power control in downlink
7.5.3	1.28 Mcps TDD option,constant BLER Target
	Performance requirements for HSDPA
9.3	Performance requirements for 1.28Mcps TDD option
9.3.1	HS-DSCH throughput for Fixed Reference Channels
9.3.2	HS-DSCH throughput for Variable Reference Channels
9.3.3	Reporting of HS-DSCH Channel Quality Indicator
9.3.4	HS-SCCH Detection Performance
5.7.1A	Error Vector Magnitude with E-DCH 16QAM
6.3A	Maximum Input Level for HS-DPSCH Reception (16QAM)